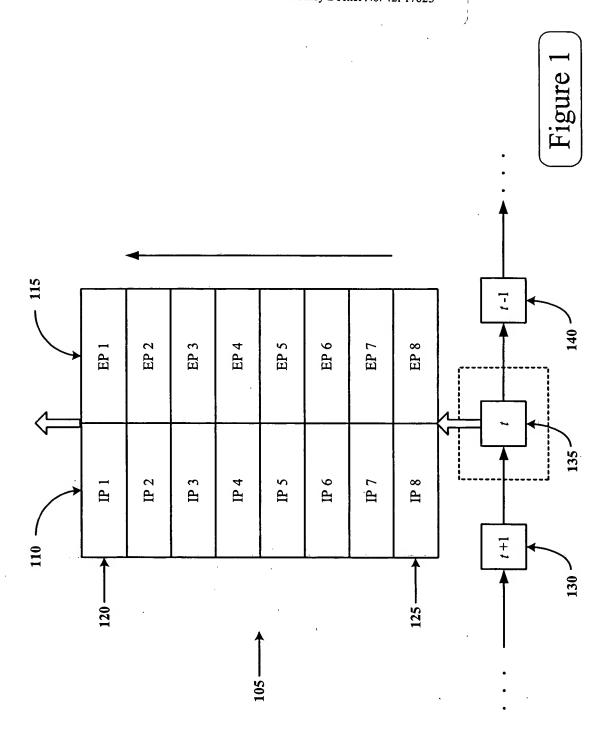
Title: Memory Trace Buffer
Inventors: Mauricio J. Serrano, et al.
Filing Date: Concurrently Herewith
Express Mail Label No.: EV 331619477 US

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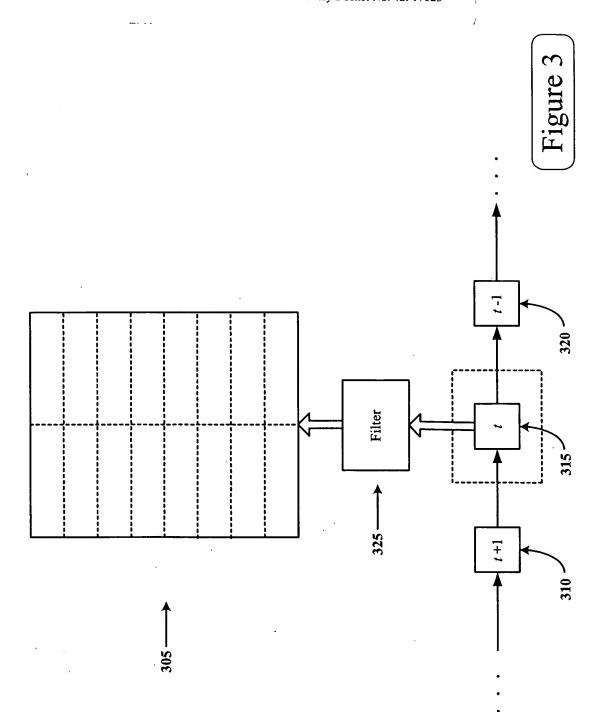
Attorney Docket No: 42P17023

240 245 Base Instruction Address			0xBCB0 MOV EDX, [EAX+8]		0xBEB0 MOV EBX, [EDX+12]			0xBAB0 MOV ECX, [EBX+EDX*4]	
235 [Effective Address]			0xBEB0 0		0xBAB0			0xABAB 0	
215 Effective Address	v0	x0	0xBCB8	x <sub>0</sub>	0xBEBC	x0	v0	0xBABA	
210 Instruction Address	IPI	IP2	IP3	IP4	IP5	IP6	IP7	IP8	
505	. ,	Ly de	220		225		,	230	ı

(303) 740-1980

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Title: Memory Trace Buffer
Inventors: Mauricio J. Serrano, et al. Filing Date: Concurrently Herewith
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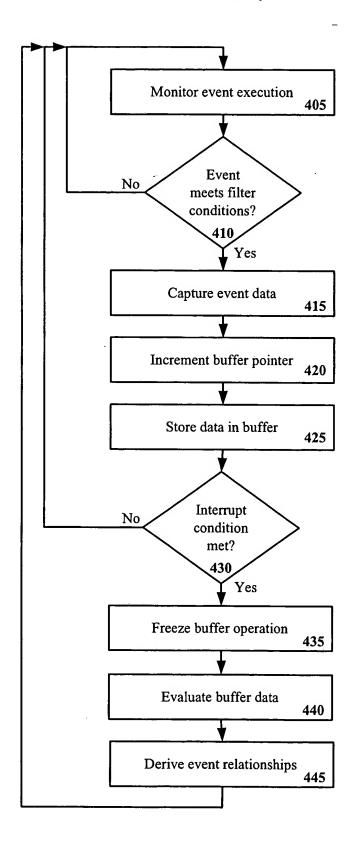


Figure 4

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Attorney Docket No: 42P17023

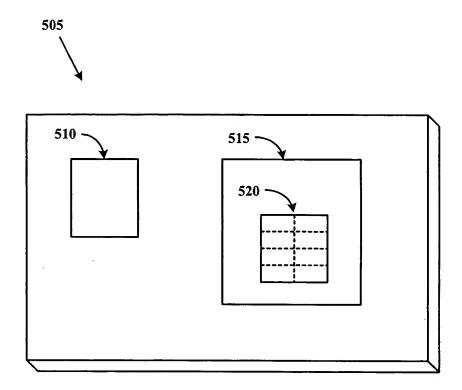


Figure 5

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